

Fig. 2

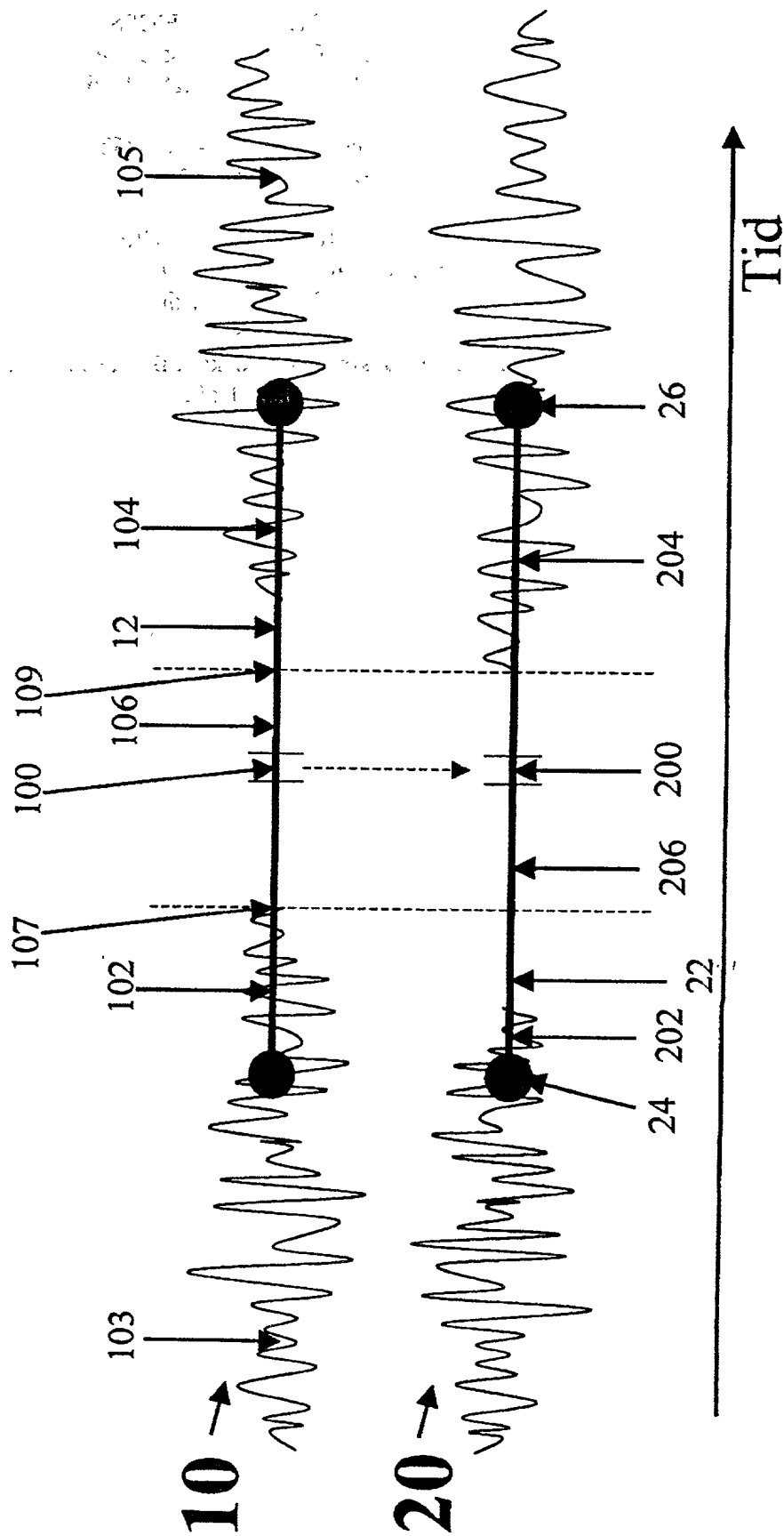


FIG. 3 is a schematic diagram of a system for measuring the time delay of a signal in a system. The system includes a signal source 704, a signal path 706, and a signal detector 708. The signal path 706 is divided into two sections, 710 and 712, by a junction 714. The signal source 704 is connected to the signal path 706, and the signal detector 708 is connected to the signal path 706. The signal path 706 is a transmission line, and the signal source 704 and signal detector 708 are connected to the signal path 706 at different points. The signal path 706 is a transmission line, and the signal source 704 and signal detector 708 are connected to the signal path 706 at different points. The signal path 706 is a transmission line, and the signal source 704 and signal detector 708 are connected to the signal path 706 at different points.

Fig. 3

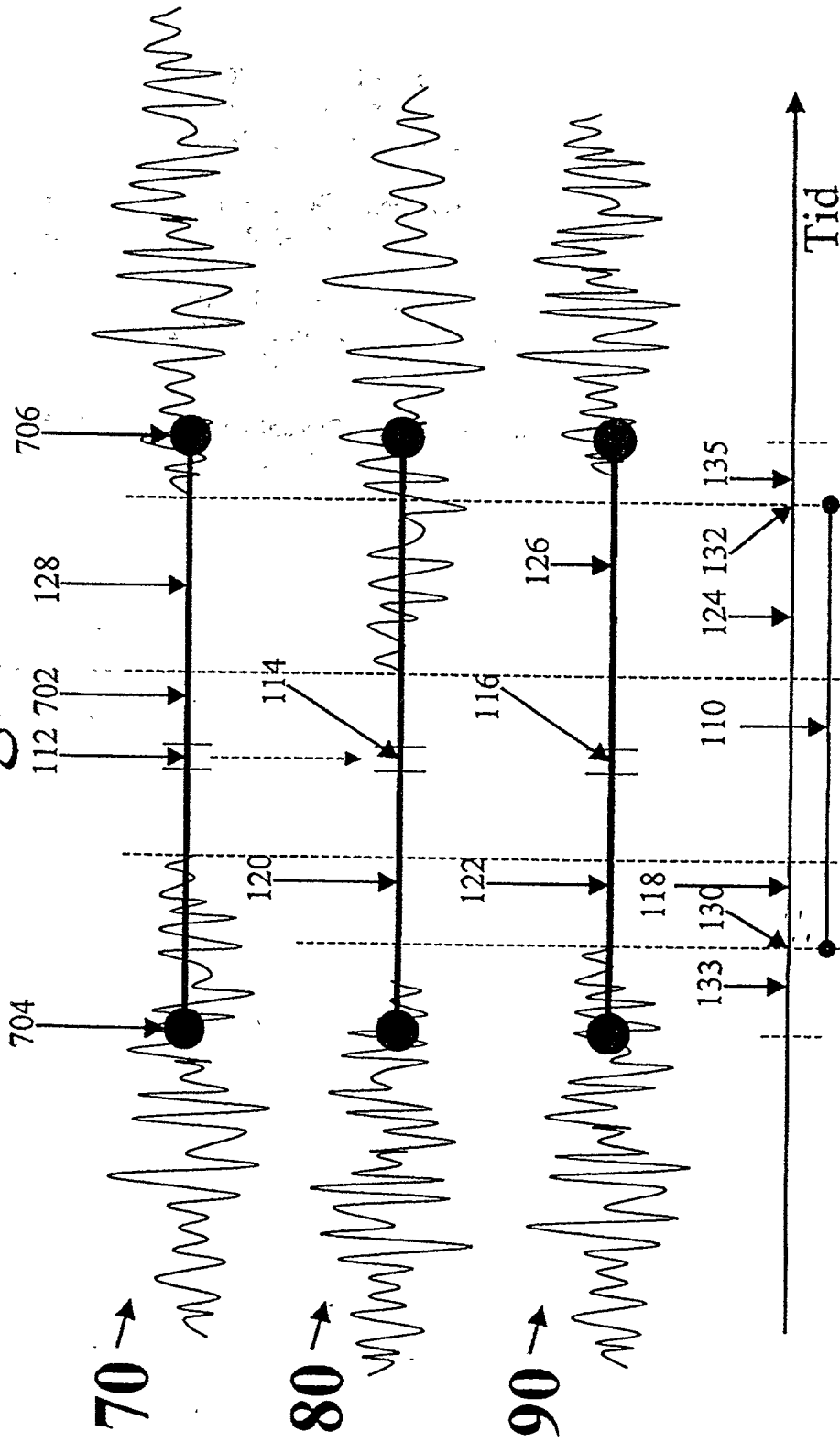




Fig. 5

